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Lab 3 Report

ECE 2031 L07

06 Feb 2022

文本

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**Figure 1.** VHDL code used to implement a rock-paper-scissors game.

图形用户界面

中度可信度描述已自动生成

**Figure 2.** A simulation waveform for all possible input and output combinations of the rock-paper-scissors game’s circuit.

图示, 示意图

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**Figure 3.** Schematic of circuit implementing f = x · /z + x · y + w · y. The worst-case propagation delay is 25 + 25 + 42 = 92 ns.

图示

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**Figure 4.** The period of the signal is 2.570 .

图示

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**Figure 5.** The positive duty cycle of the signal is 1.745 / 2.570 = 67.9 %.

图表, 折线图

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**Figure 6.** The fall time of the signal is 2.50 ns.

图表, 折线图

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**Figure 7.** The High-to-low propagation delay is 25.95 ns.

图表, 折线图

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**Figure 8.** The Low-to-high propagation delay is 19.55 ns.